Docket No.

460

251173US2CONT

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Tadashi MATSUDA

SERIAL NO: New Continuation Application

GAU: /

FILED:

Herewith

**EXAMINER:** 

FOR:

INSULATED GATE BIPOLAR TRANSISTOR

# INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

### REFERENCES

- Copies of the references listed on the attached form PTO-1449 were provided in prior application serial number 10/298,514, filed November 19, 2002, now pending, as were either statements of relevancy and any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

#### RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

### **CERTIFICATION**

- Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

### DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number <u>15-0030</u>. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

Registration No. 28,870

Katherine D. Pauley Registration No. 50,607

Customer Number 22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

Form DTO 4440	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY DOCKET NO.		SERIAL NO.		
Form PTO 1449 (Modified)				251173US2CONT	New Continuation Application			
				APPLICANT				
LIST OF	REFER	RENCES CITED BY AP	PLICANT	Tadashi MATSUDA				
				FILING DATE	GROUP			
				Herewith				
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB FILING DATE CLASS IF APPROPRIATE		
	AA	5,329,142	7/12/94	Mitsuhiko KITAGAWA et al.				
	AB							
	AC		Ì					
	AD							
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			FC	PREIGN PATENT DOCUMENTS				
		DOCUMENT NUMBER	DATE	COUNTRY		TRANSLATION YES NO		
	AO							
	AP							
	AQ							
	AR							
	AS							
	AT							
	AU							
	AV							
		OTHER R	EFERENCES	(Including Author, Title, Date, Pertine	ent Pages,	etc.)		
Mitsuhiko KITAGAWA et al, "A 4500 V INJECTION ENHANCED INSULATED GATE BIPOLAR TRANSISTOR (IEGT)  OPERATING IN A MODE SIMILAR TO A THYRISTOR," IEDM Technical Digest, International Electron Devices Meeting, December 5-8, 1993, pgs. 679-682.								
	AX							
	AY							
	AZ				Additional References sheet(s) attached			
Examiner				Date Considered				
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								